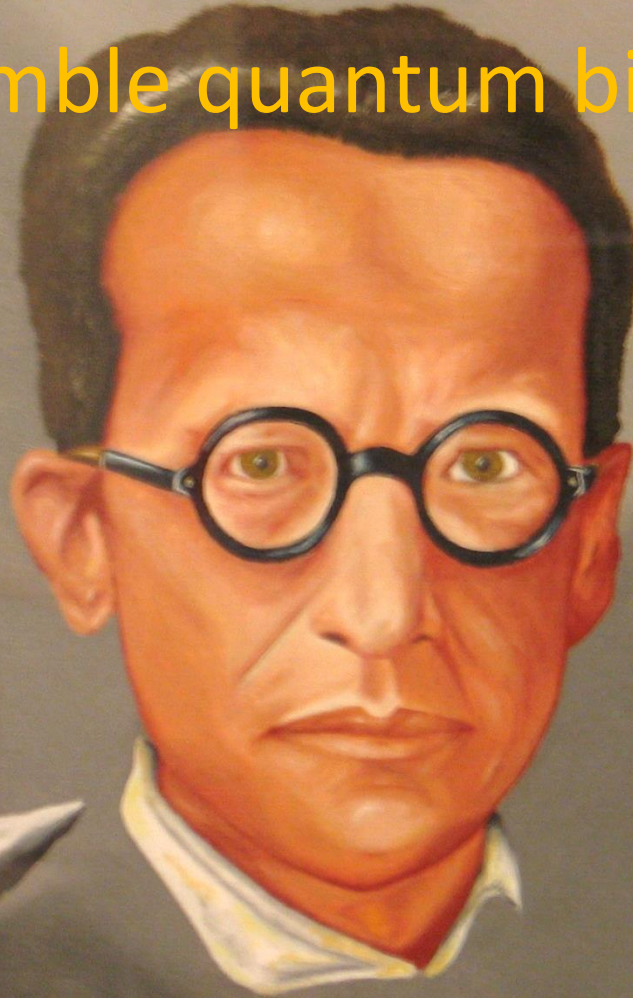


# Quantum Computing: how to fabricate and assemble quantum bits



Saclay ESNT workshop:  
QC&Nuclear Physics  
June 12-14 2019

Daniel ESTEVE

QUANTUM  
ELECTRONICS GROUP



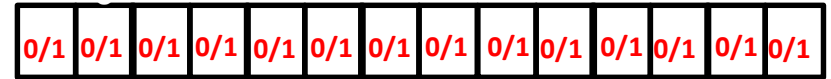
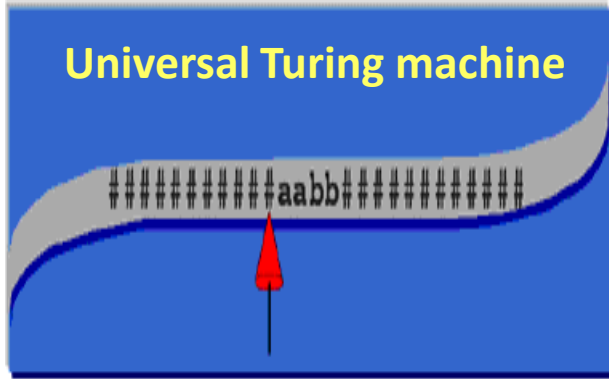
ED. C. 04

# Quantum computing: the origin

Classical computing

Computer:

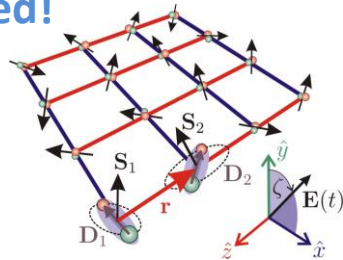
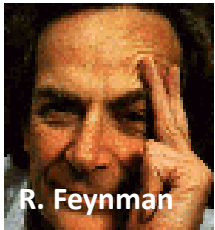
$n$  (0,1) bits evolve among  $2^n$  states



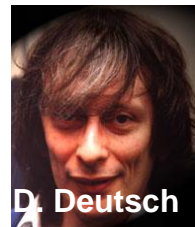
$$R = (i_1, i_2, i_3 \dots i_{2^N}) \quad i_k = 0, 1$$

But is a universal Turing machine truly universal ?

1982: Quantum systems too hard to crack, quantum simulation needed!



1985 the breakthrough:



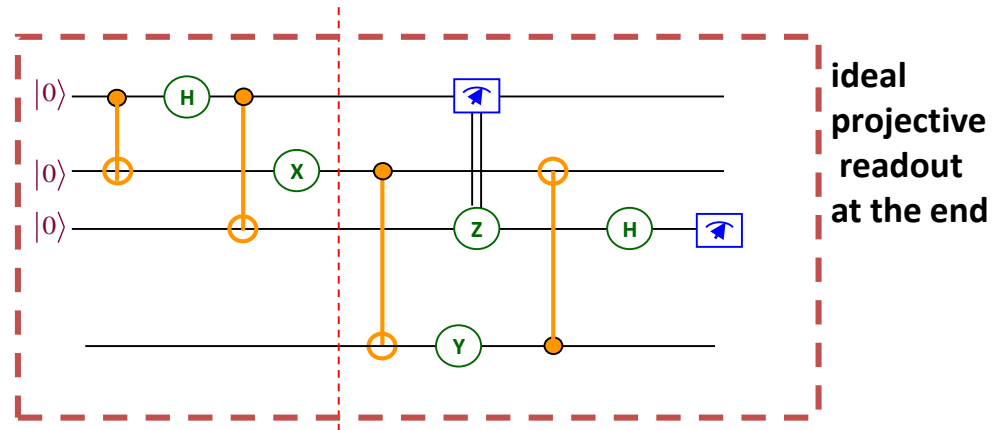
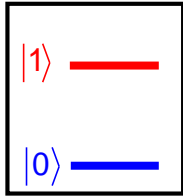
Quantum mechanics provides computational resources !

# The art of Quantum computing

Unitary evolution of a  $n$  quantum bit register  
qubit

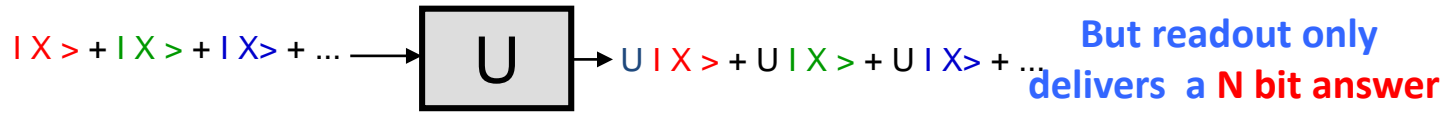
$2^n$  computational  
basis states

$$\overbrace{|010001\dots 1\rangle}^n = |p\rangle$$

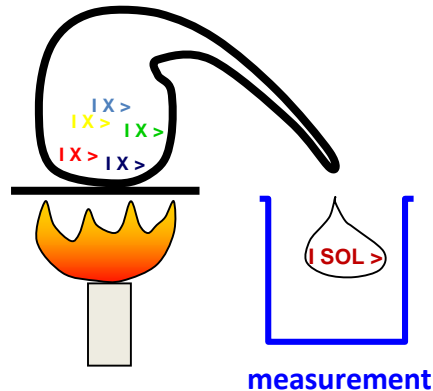


$$\sum_{i_k=0,1} a_{i_1 i_2 i_3 i_4 \dots i_{2^N}} |i_1, i_2, i_3 \dots i_{2^N}\rangle \text{ (entangled state)}$$

QM being linear  
the evolution can be  
massively parallel



QC: the art of obtaining a useful answer at measurement time



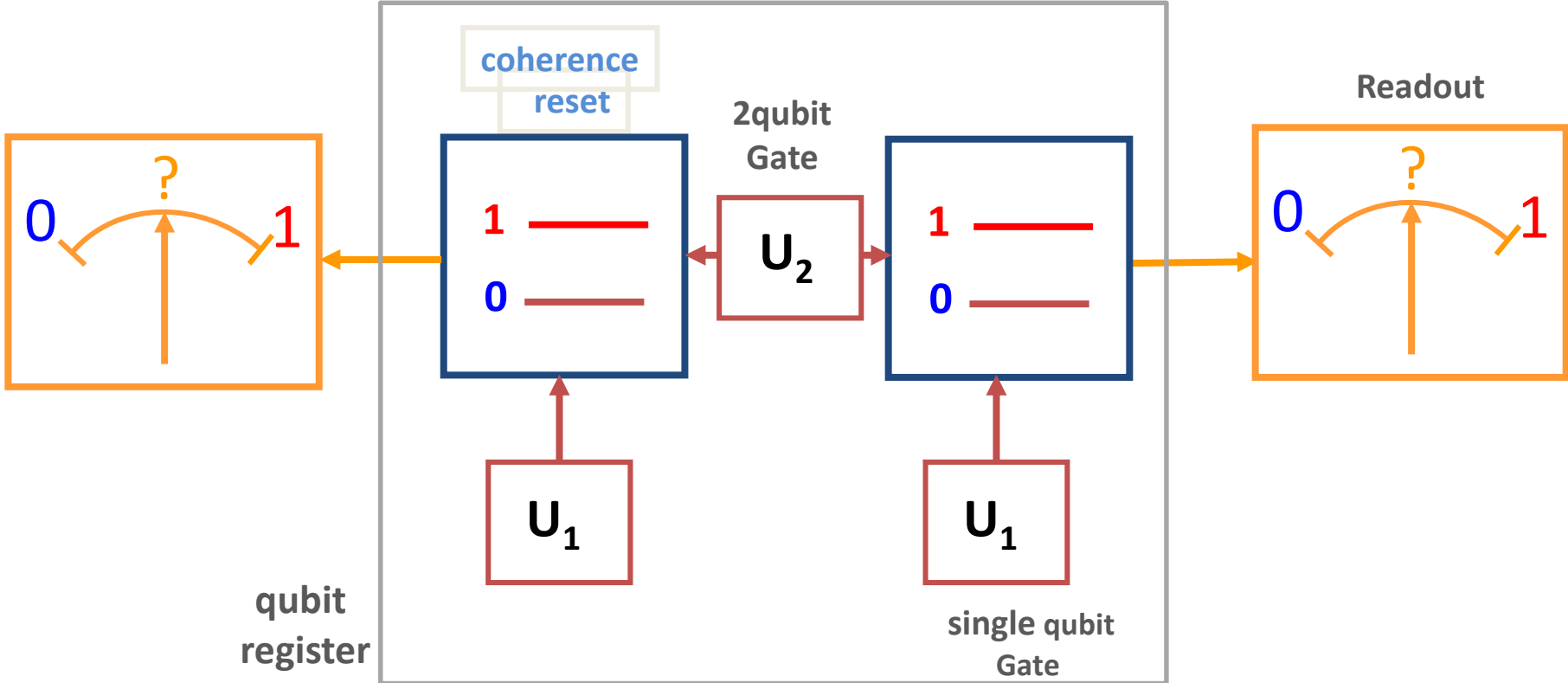
A 50-100 **ideal qubit** quantum computer  
would overcome classical computers  
(for some already interesting tasks)

# blueprint of a quantum processor (based on quantum gates)

qubits  
(2 level systems)

Universal set of  
unitary gates

high fidelity  
projective readout

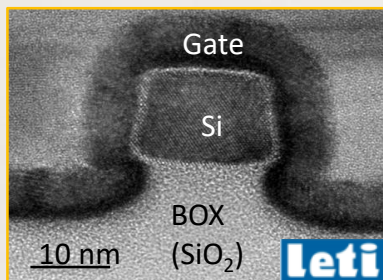


"DiVincenzo criteria"

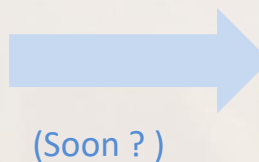
# The hpc context:

## massive integration progresses at a slower pace

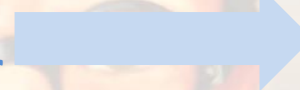
Semiconducting circuits reach physics limits  
+ increasing needs



Ultimate CMOS



3D stacking  
ultra low power



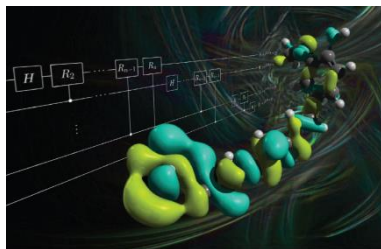
Alternative  
technologies  
for HPC ?

Quantum computing  
now envisioned

# QC: A potential breakthrough in HPC (?)

## Use-cases

Many-body physics:  
quantum chemistry, materials  
**nuclear physics ...**



Fermionized Hamiltonians  
map well on qubits

**needs: >100 qubits**

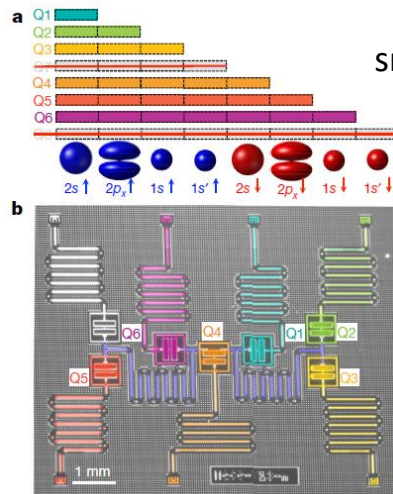
Linear algebra:  
quantum inversion of  
sparse matrices

HHL algorithm

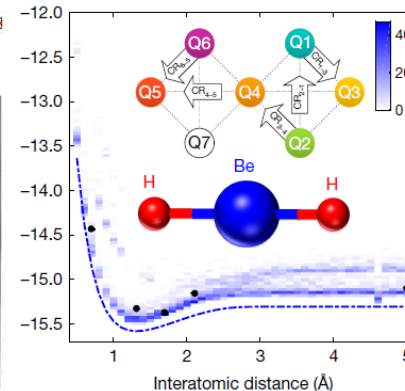
Quantum machine learning

Harrow, Hassidim, Lloyd, PRL 103, 150502 (2009)

Gao, Zhang, Duan arXiv:1711.02038



small scale demos:



**variational quantum eigensolver :**  
**IBM Kandala et al., Nature 549, 242 (2017)**

**quantum RAM needed !**

**quantum RAM needed !**

Big players attracted, strong partnerships developed

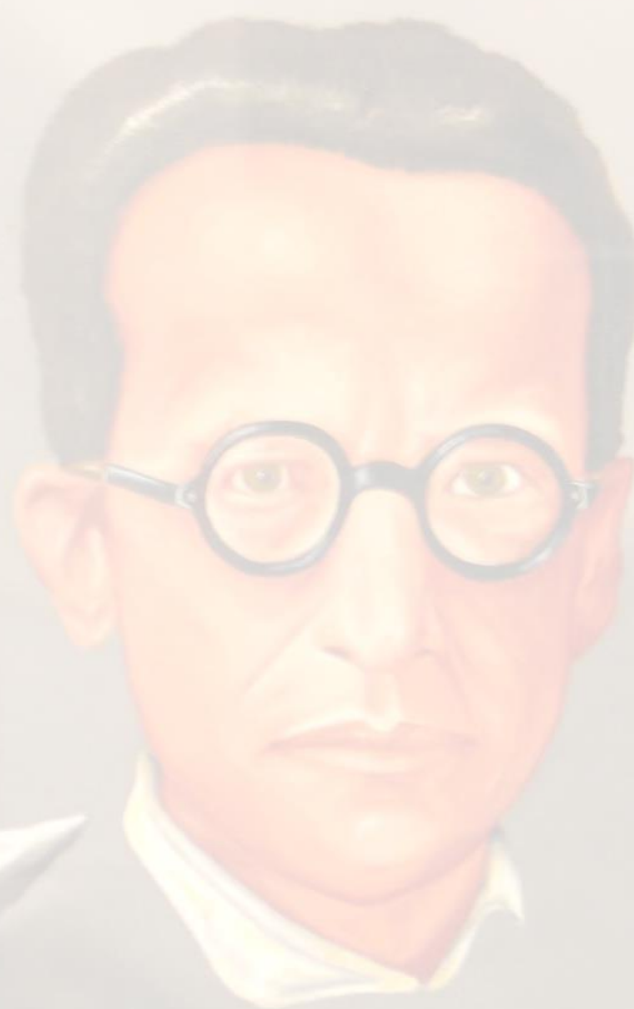


Eu:



"Strong" Eu flagship initiative

# Qubit systems ?

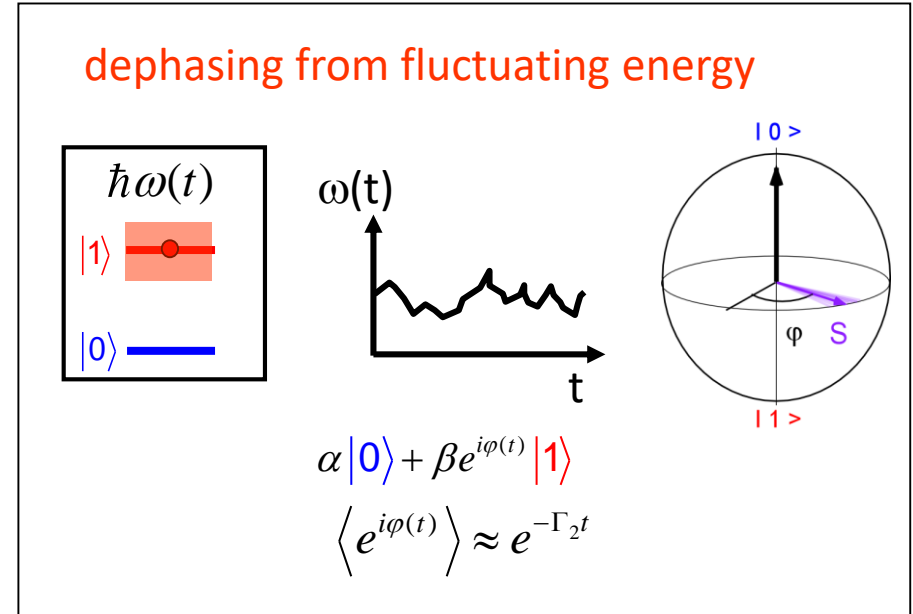
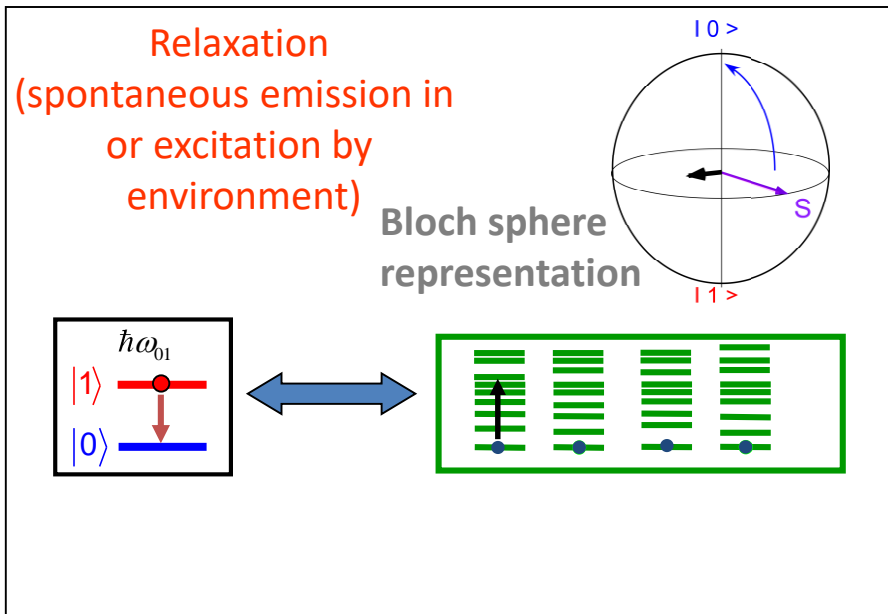


ED. C. 04

# Can any quantum system be a quantum bit ?

The issue: coupling to environment yields **decoherence**

cf Ithier et al., PRB 72, 134519, 2005



**Microscopic systems**  
weakly coupled to their environment  
quantum regime easy  
not easily addressable

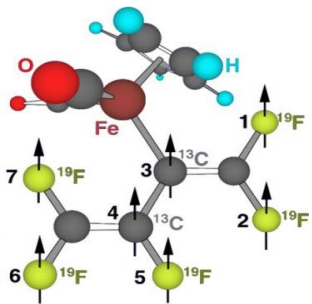
**Macroscopic systems**  
strongly coupled to their environment  
quantum regime difficult  
easily addressable

**solutions ?**



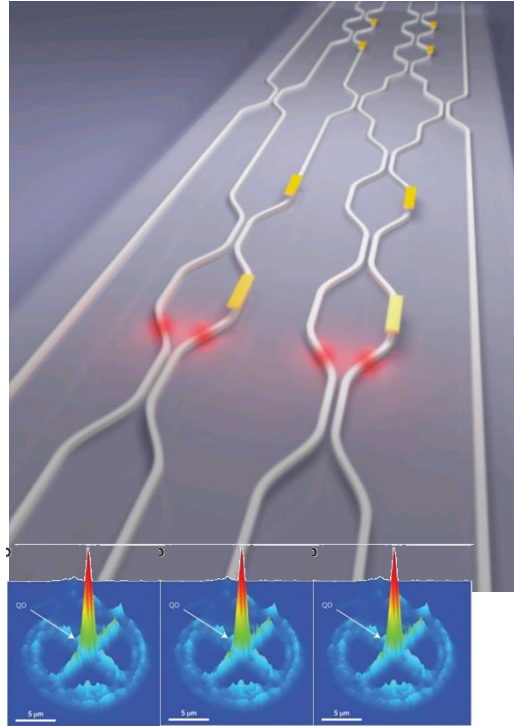
# Physical implementations

## NMR



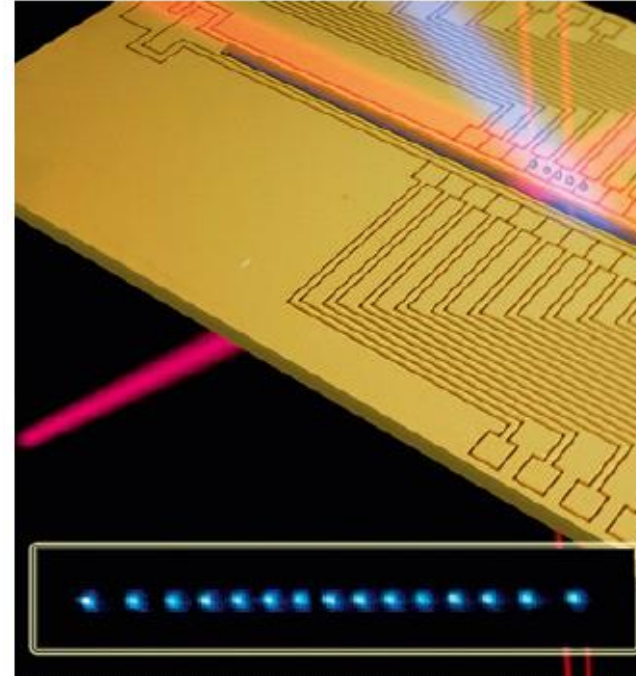
code=molecules  
**not** scalable

## Photons



gates yet not achieved  
Other strategy:  
measurement based Qcomp  
with **cluster states** made  
using identical photons

## Trapped ion 1D-2D arrays

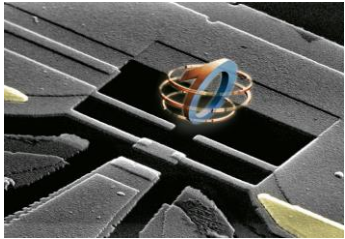


The most advanced platform  
scalability problematic  
... Electrical circuits ?

# Electrical qubit circuits (non exhaustive)

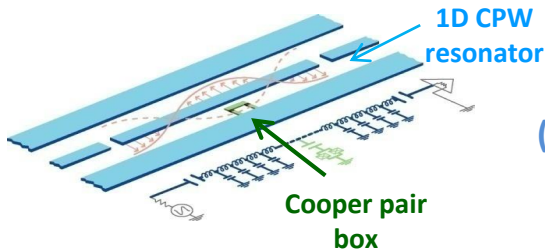
quantum states of superconducting circuits

Superconducting qubits based on the Single Cooper Pair Box circuit



functional SC qubit (CEA 2002)

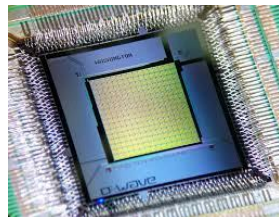
Now: Transmon type Cooper Pair Box circuit



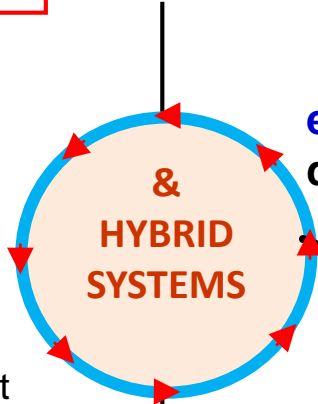
Yale (2004-2006-2010)

Different computing strategy quantum annealing

difficult problems solved  
quantum speed-up not demonstrated



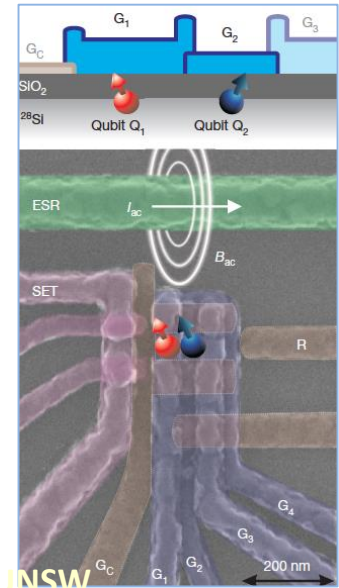
DWAVE



Electron spin states in semiconductor structures

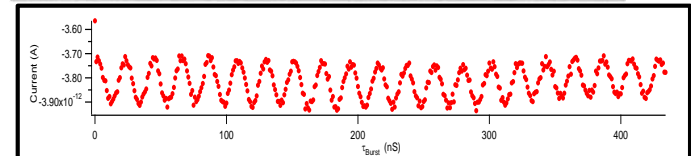
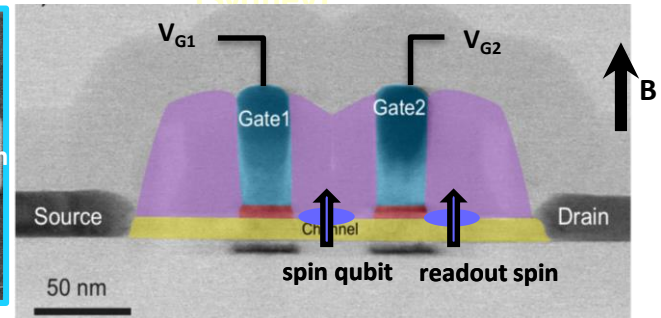
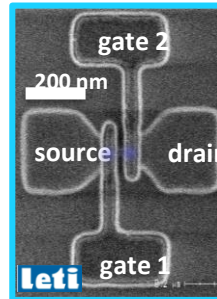
e spins in quantum dots

UNSW, TUDelft, Harvard, CEA (INAC-LETI)



UNSW (Sydney)

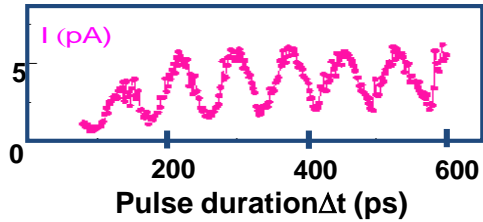
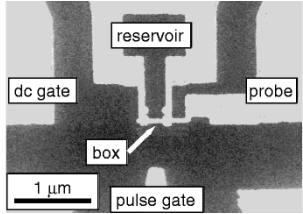
a qubit from on an industrial fab line at CEA



# The Cooper Pair Box quantum bit: a brief survey

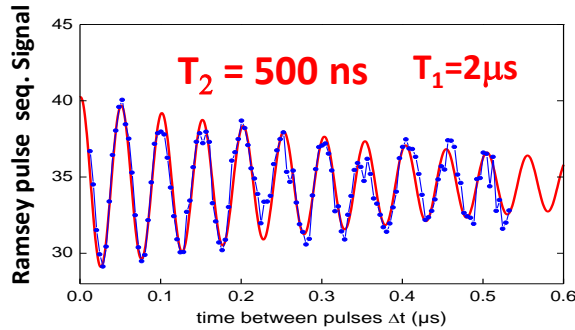
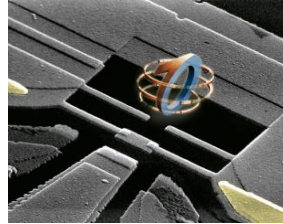
## first Cooper Pair Box qubit

Nakamura, Pashkin & Tsai (NEC, 1999)



## First operational qubit

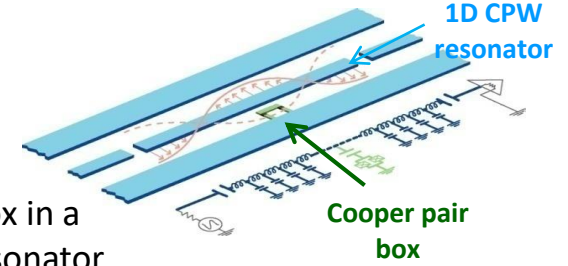
Vion et al., (Quantronics, 2002)



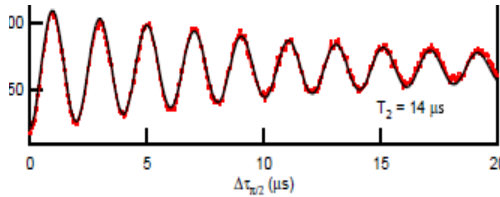
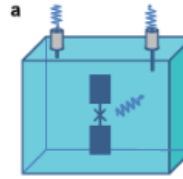
## Circuit QED: transmon Cooper pair box in a microwave cavity (2D, 3D)

Schoelkopf lab., Yale ; Wallraff et al., Nature 2004

-Koch et al., PRB 2007; Paik et al., PRL 2011

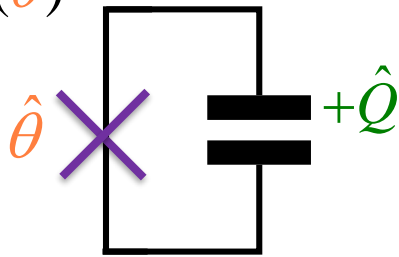
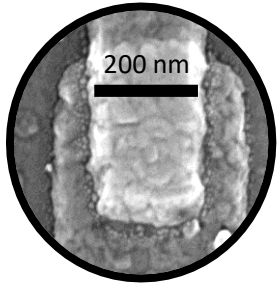


CPBox in a 3D resonator



## The transmon

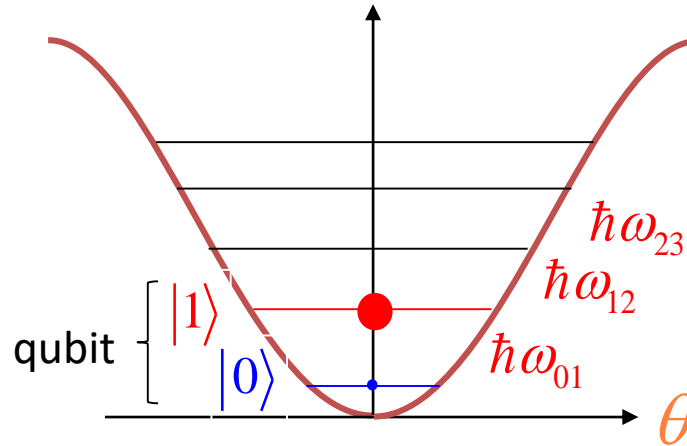
$$E_p = -E_J \cos(\theta)$$



Josephson junction Phase :  
Flux across junction

$$\hat{\Phi} = \left( \frac{\hbar}{2e} \right) \hat{\theta}$$

Al/Al<sub>2</sub>O<sub>3</sub>/Al



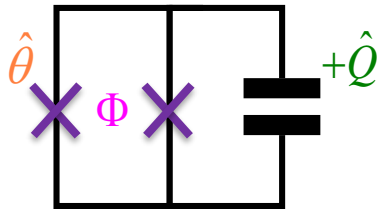
anharmonic oscillator

$$\hat{H} = -E_J \cos(\theta) + \frac{\hat{Q}^2}{2C}$$

# Circuit-QED architecture to readout a Josephson qubit

flux tunable qubit

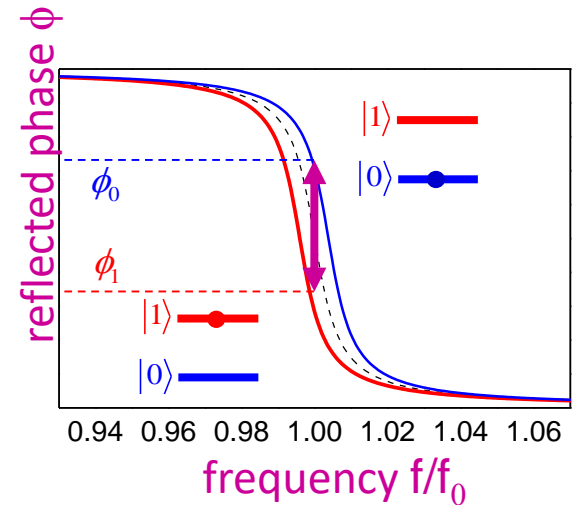
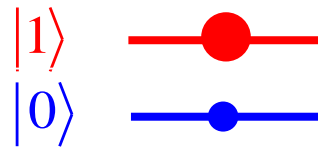
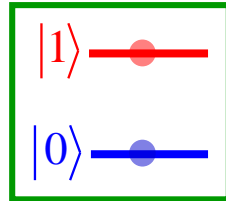
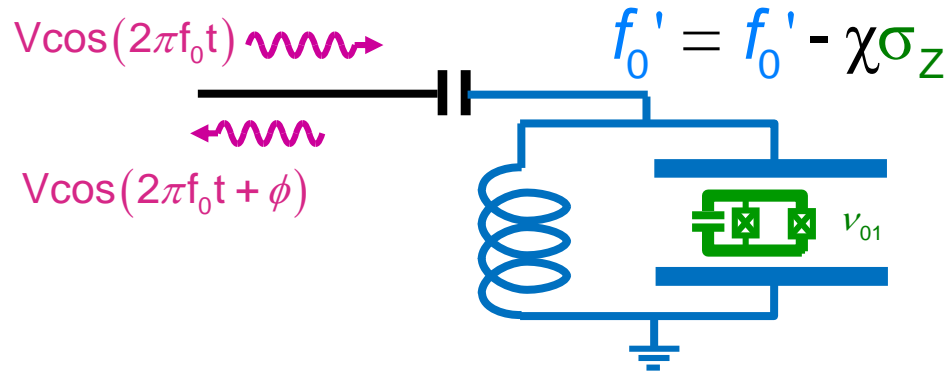
$$L_J(\theta, \Phi)$$



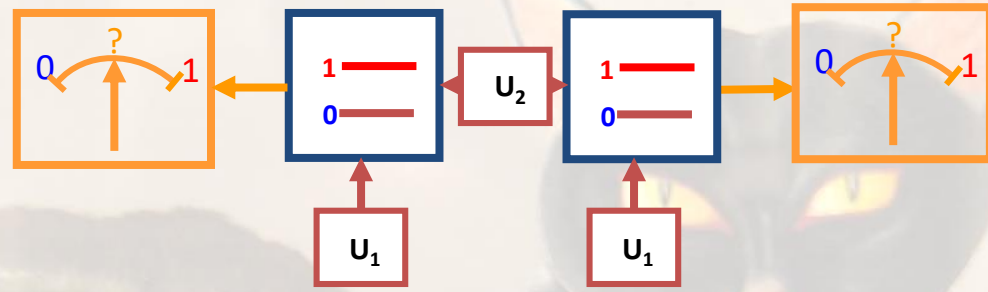
$$E_p = -E_J \cos(\theta)$$

$$\hat{H} = -\hbar\omega_{01}(\Phi)\hat{\sigma}_z/2$$

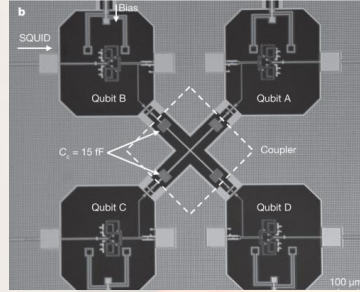
Dispersive readout with a resonator :



# Running quantum algorithms on elementary processors

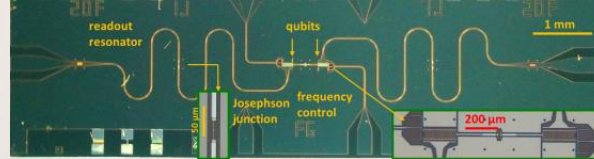


Martinis Lab, UC Santa Barbara  
Yamamoto et.al. ,  
PRB 82 2010 , Nat Phys 2012



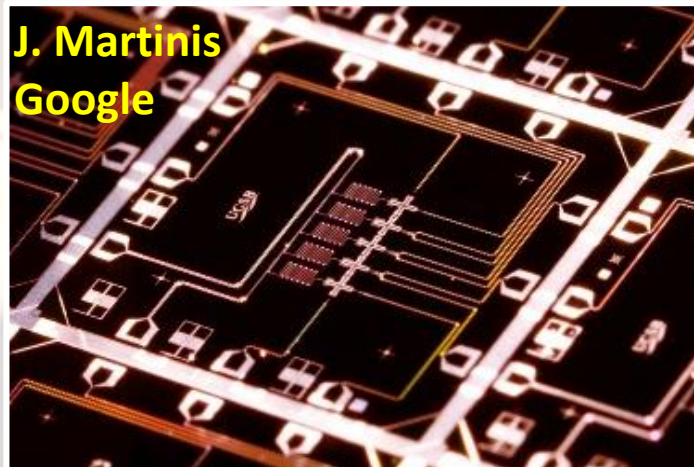
*Shor factorization  
algorithm*

Quantronics, CEA  
Dewes et. al., PRL & PRB 2012



*Grover search  
algorithm*

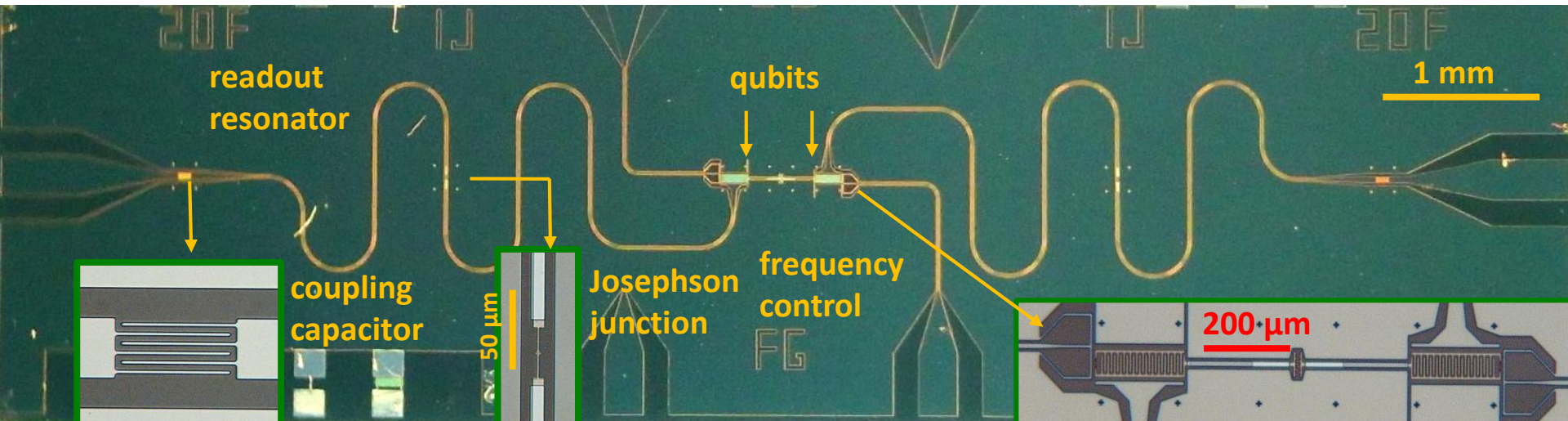
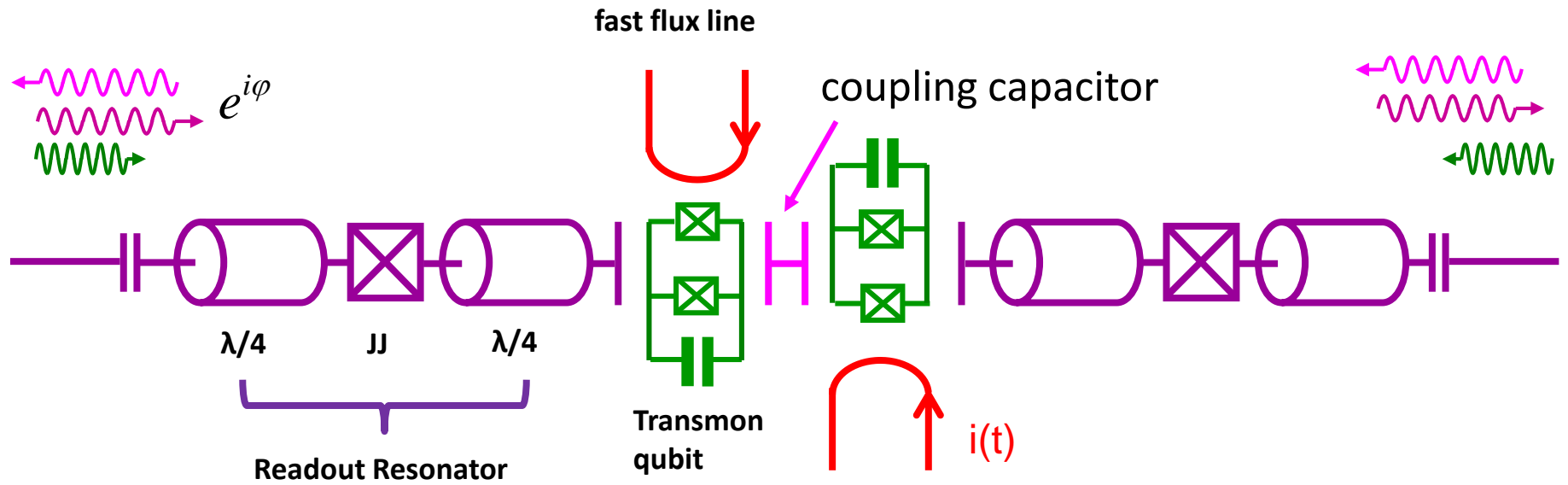
Martinis lab, Google 2015...  
IBM, TUD



*correcting  
some errors*

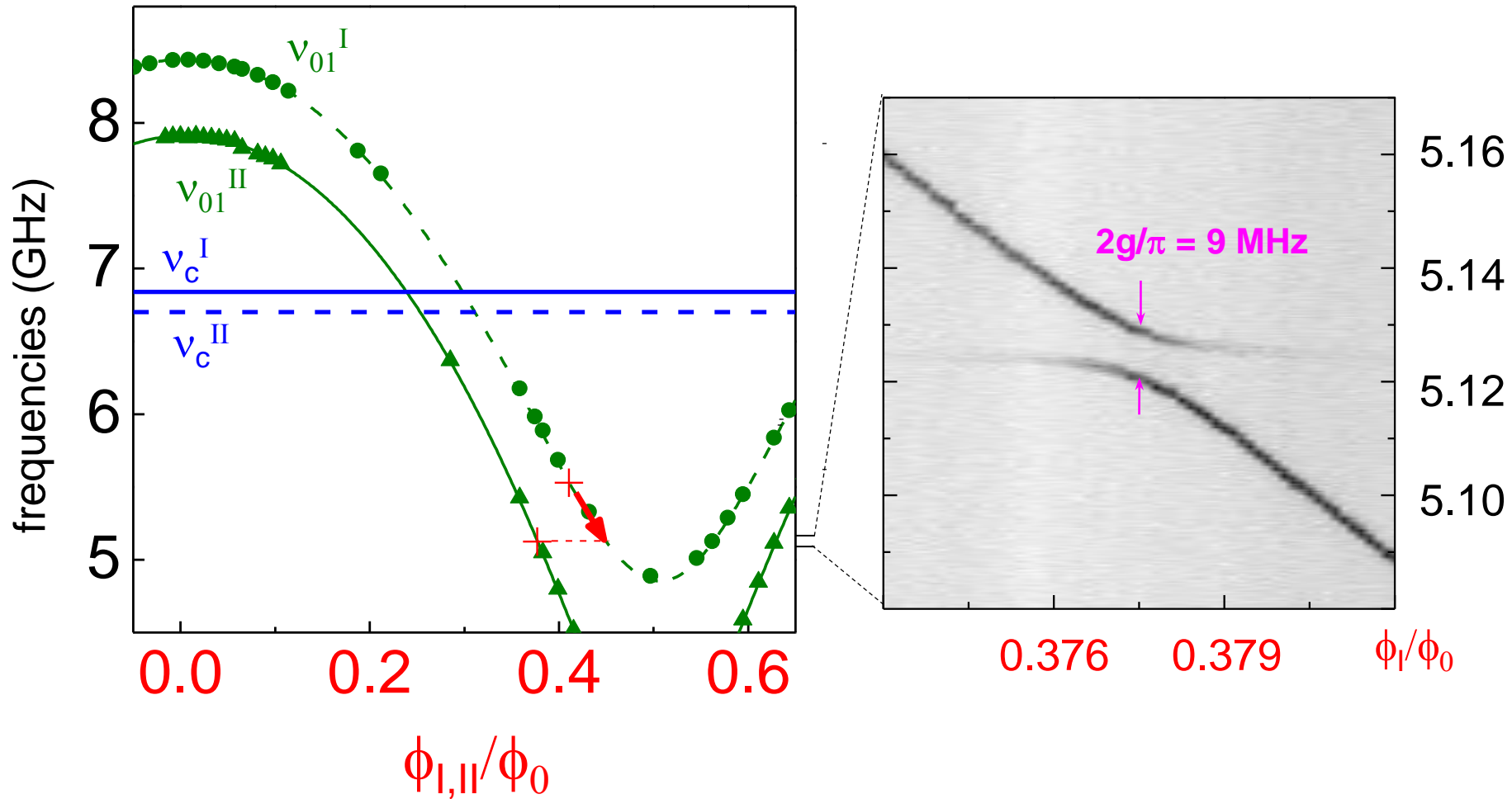
# The simplest case: a two-transmon processor

Dewes et al., Phys. Rev. Lett. 108, 057002 (2012)

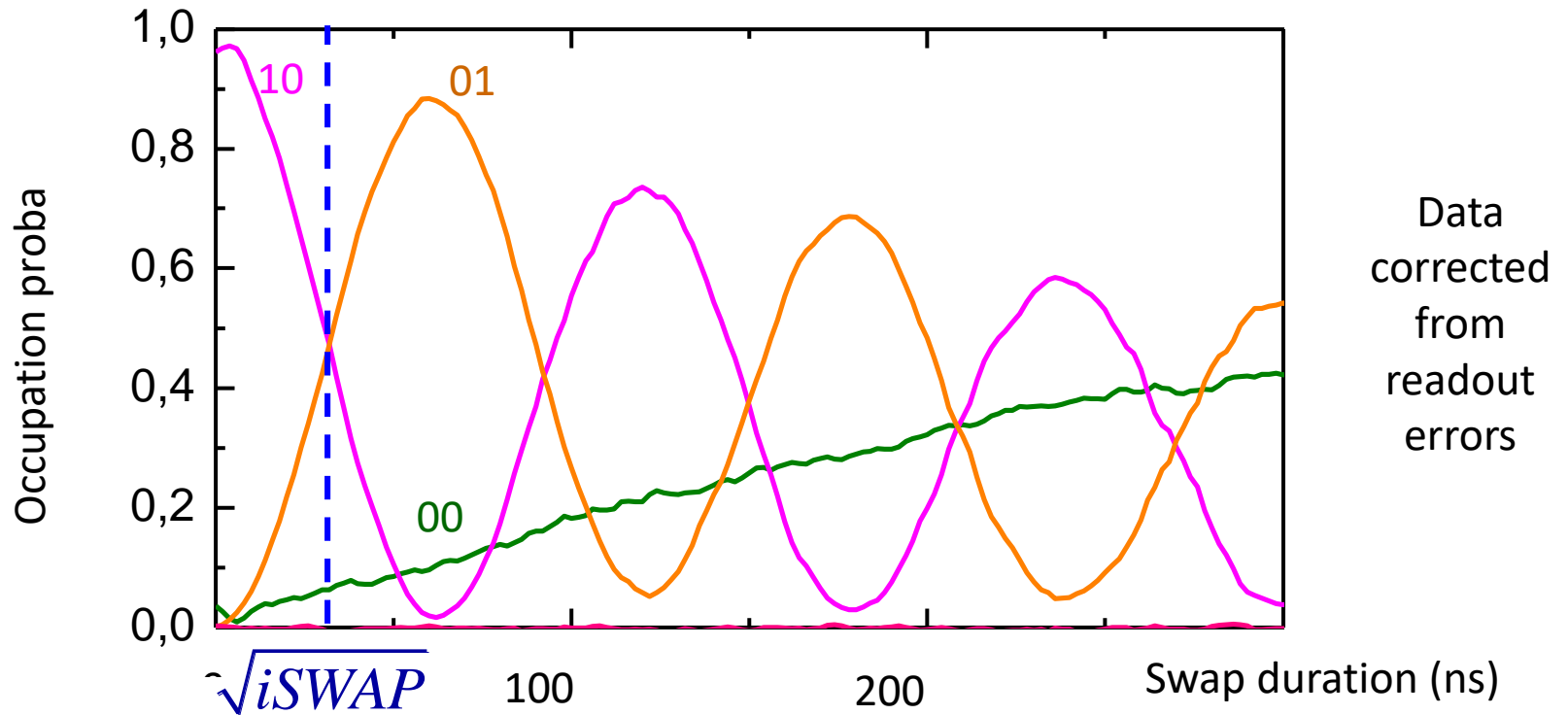
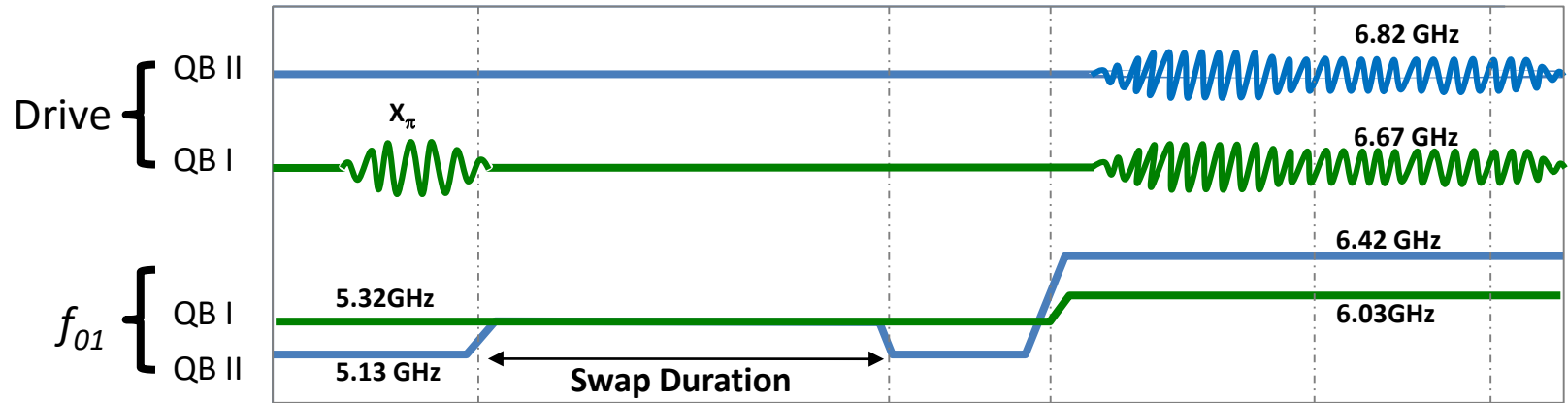


## 5. Simple example of the $i\text{SWAP}^{1/2}$ two-qubit gate

A. Dewes et al., PRL (2011)



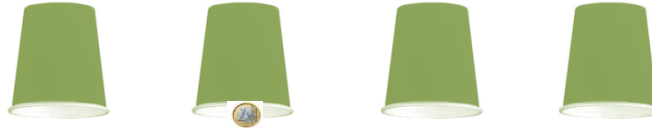
## 4. Simple example of the $i\text{SWAP}^{1/2}$ two-qubit gate





# The Grover search algorithm on 4 objects

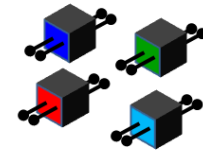
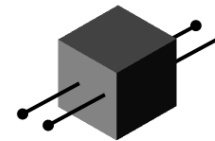
*the search problem*



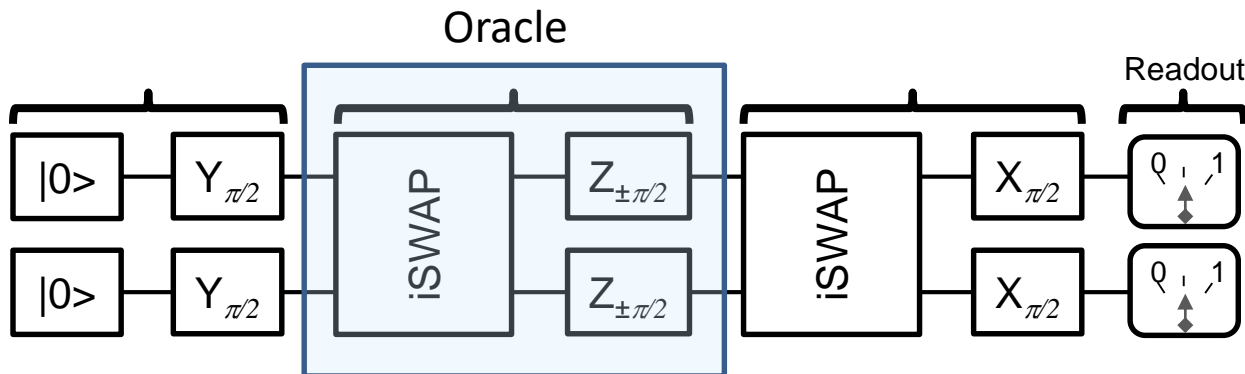
Classical search:  $O(N)$  steps    Quantum search :  $O(\sqrt{N})$  steps

4 object benchmark case: **1 try enough !**

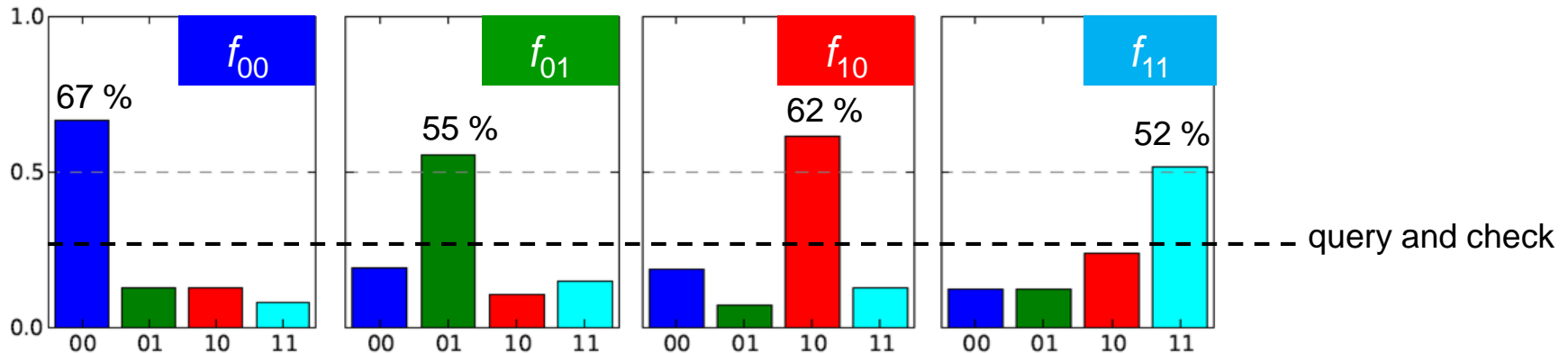
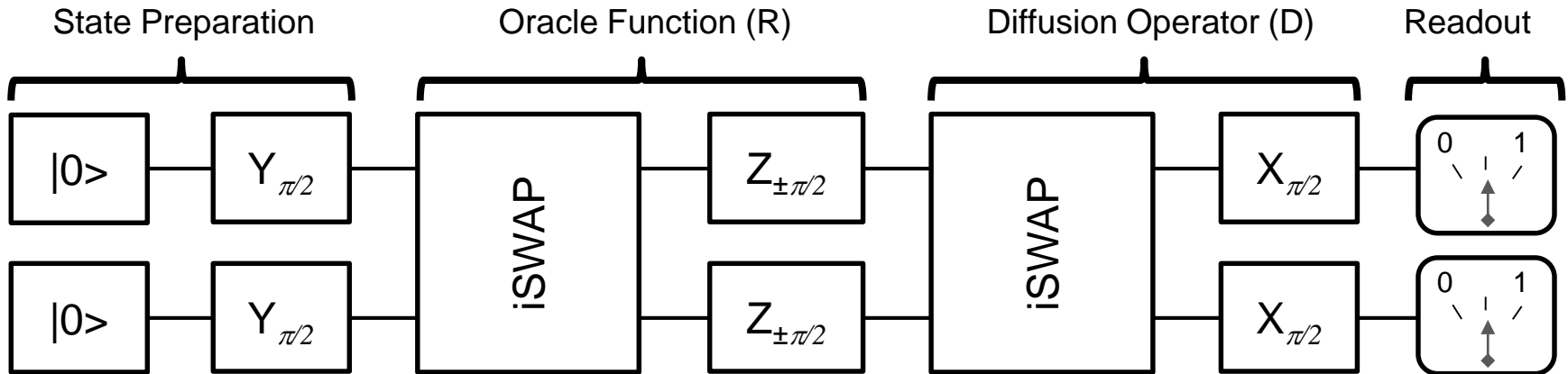
Oracle marking a state



$$i \in \{00 \quad 01 \quad 10 \quad 11\}$$



# The Grover search algorithm: success probability

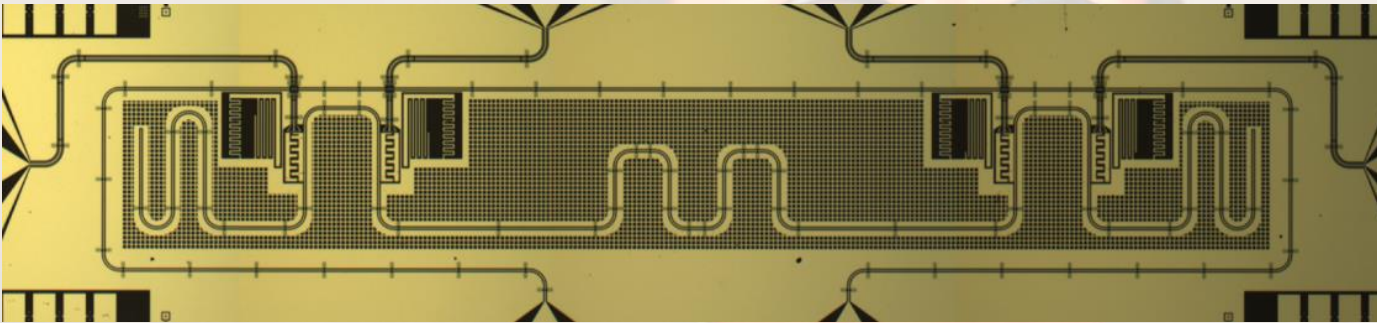


$F_i > 25\% \rightarrow$  Quantum speed-up achieved!

# Scaling up ?

4 qubit processor (2015) :

multiplexed readout  
2 qubit gates through common bus  
& did not work well



**Issue:**

A route that does not solve  
the scalability challenge

# The scalability challenge

## GATE BASED PROCESSORS

### Elementary processors

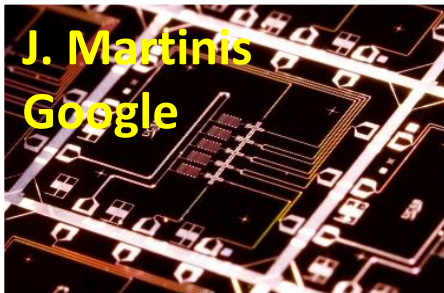
Better coherence:

$T_2$ : 10-30  $\mu$ s

Protocols :

teleportation,  
quantum feedback  
Digital simulation

### The NISQ era



Quantum advantage  
within reach ?

## Addressing Quantum Error Correction

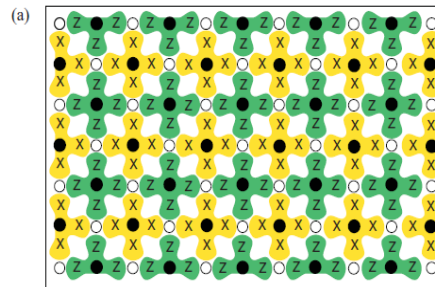
Beware: copying forbidden!

fault-tolerant architectures

surface code fabric

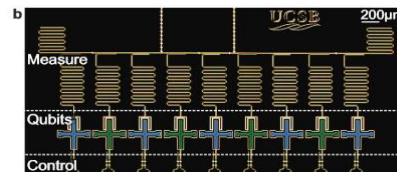
(see Fowler et al, PRA 86 (2012))

Data  $\circ$  measurement  $\bullet$



Huge resource overhead

1 logical qubit  $\gg$  1000 physical qubits



test circuits

for quantum error correction

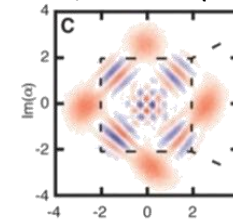
robust qubits

## Dissipation engineering

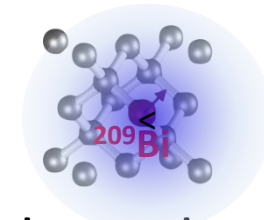
Yale Quantlab,  
INRIA- LPA (ENS)

ENS Lyon (B. Huard)  
Schrödinger cat states in  
high Q resonators  
Mirrahimi et al.

NJP 16, 32014 (2014)



## hybrids: spins in circuits



better coherence

# Quantum Supremacy

## QUANTUM COMPUTING AND THE ENTANGLEMENT FRONTIER

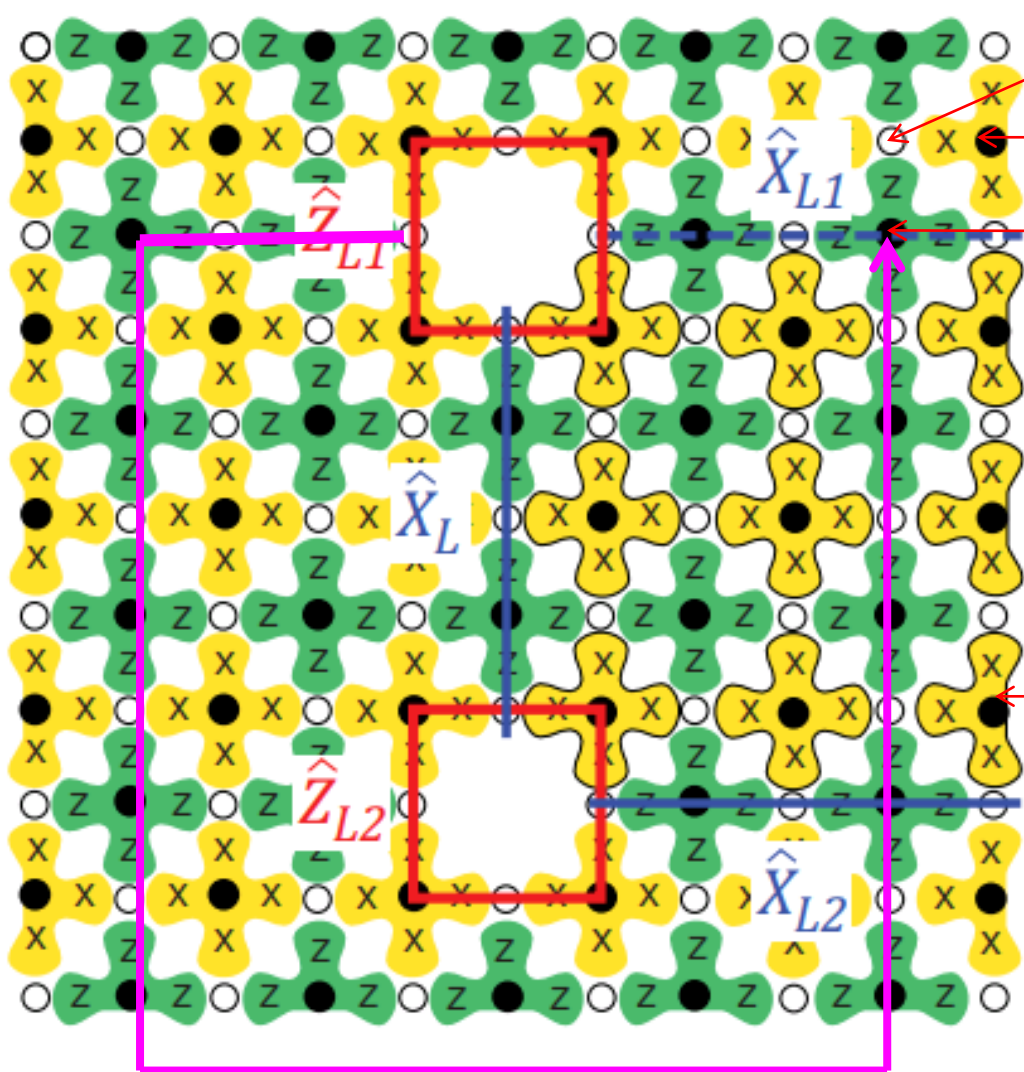
JOHN PRESKILL

*Quantum advantage ?*

We therefore hope to hasten the onset of the era of ~~quantum supremacy~~, when we will be able to perform tasks with controlled quantum systems going beyond what can be achieved with ordinary digital computers. To realize that dream, we must overcome the formidable enemy of *decoherence*, which makes typical large quantum systems behave classically. So another question looms over the subject:

*Is controlling large-scale quantum systems merely really, really hard, or is it ridiculously hard?*

# The surface (stabilizer) code architecture



data qubit

X measure qubit

Z measure qubit

- Repeat measurements of all X,Z

- Memorize errors

- don't need to correct

- Define plaquette logical qubit

- Define **logical qubits by holes**

- Single qubit gate  $X_{L1}$  = product X

- Two qubit gates by braiding

But ... huge overhead: >3600 qubit/logical qubit  
@ 0.1% error/gate !

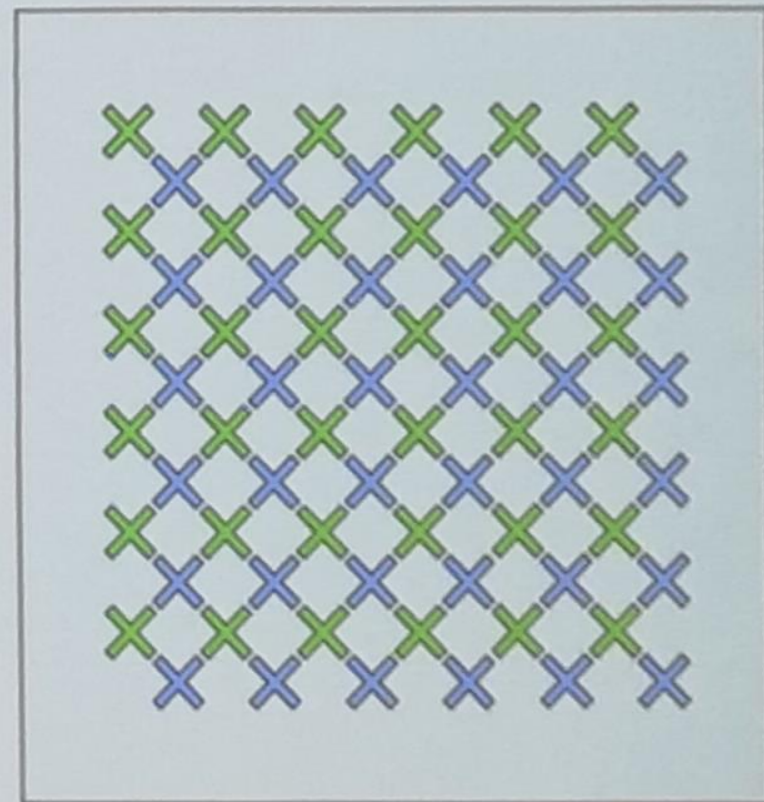
**Scalable fabrication  
mandatory!**

## "Bristlecone" a true 2-D grid



### Bristlecone

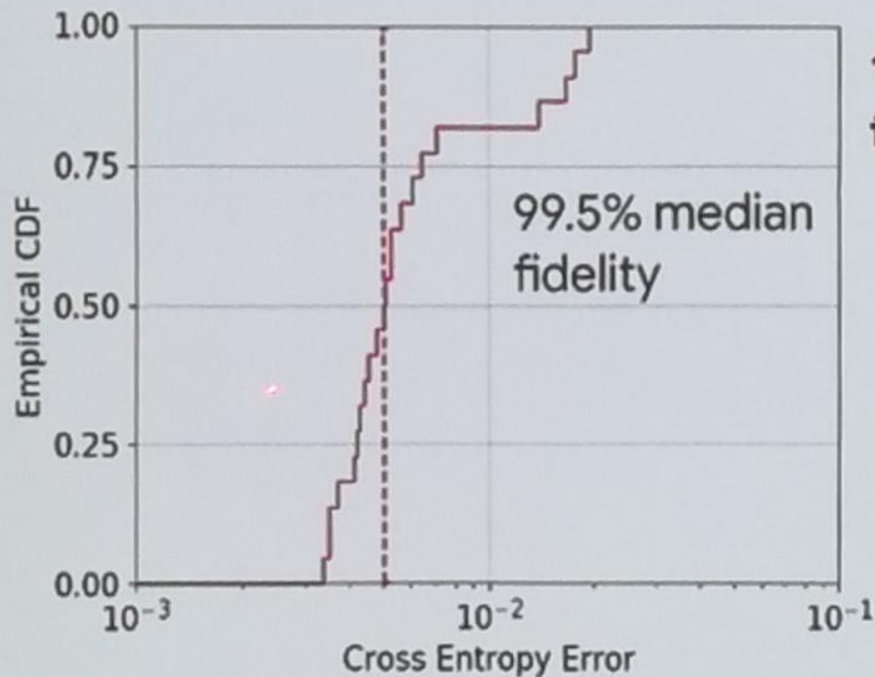
- 12 unit cells of 6 qubits = 72
- Flux tunable transmon qubits
- Fixed capacitive coupling
- Enough for "Quantum Supremacy"
- Enough for 1st, 2nd order Surface Code
- Starting point for near-term algorithms



does not work well, but...

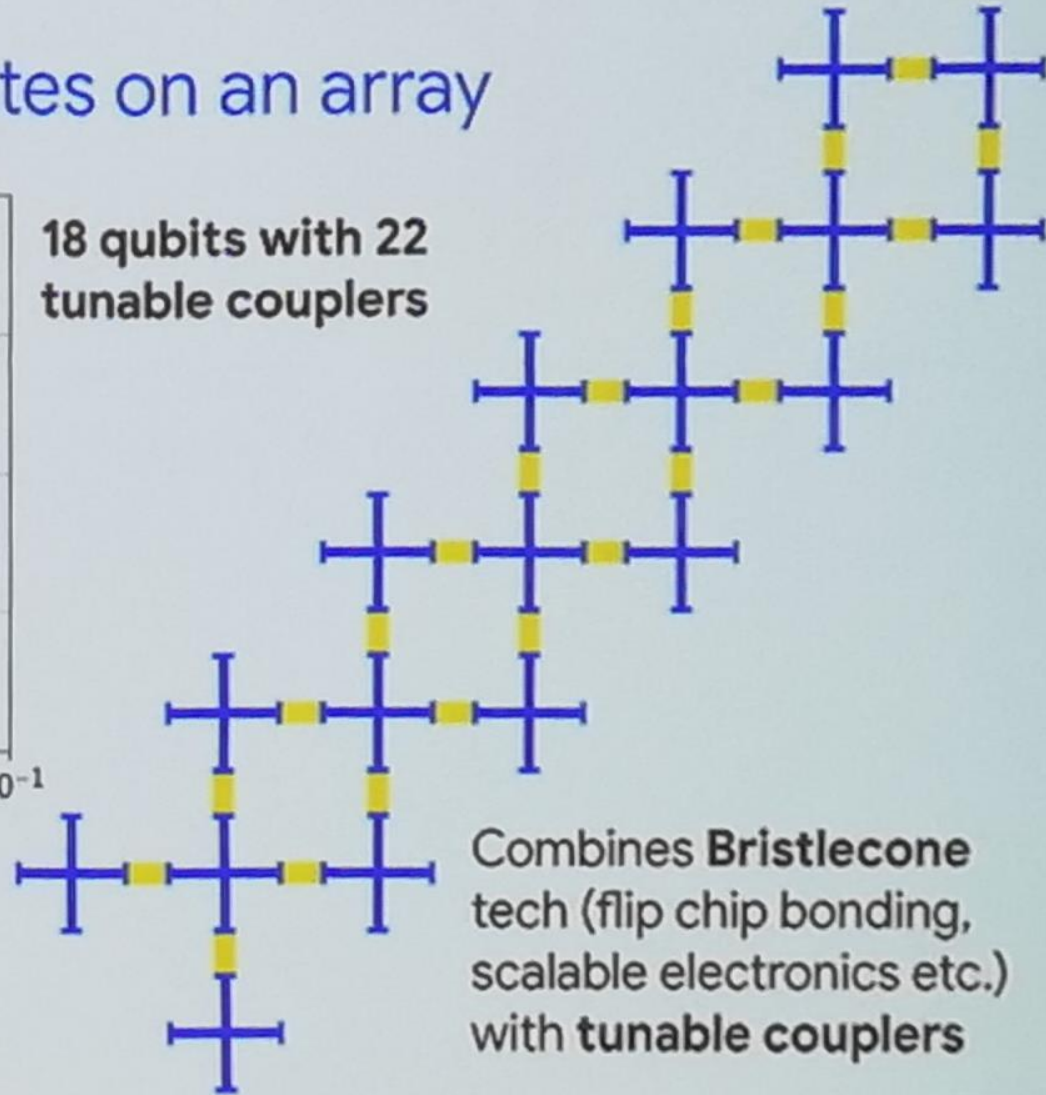
# Significant progress achieved using tunable couplers

## High fidelity 2-qubit gates on an array



**Note:** Single gate for each pair of qubits, not whole subspace

18 qubits with 22 tunable couplers



Combines **Bristlecone** tech (flip chip bonding, scalable electronics etc.) with **tunable couplers**

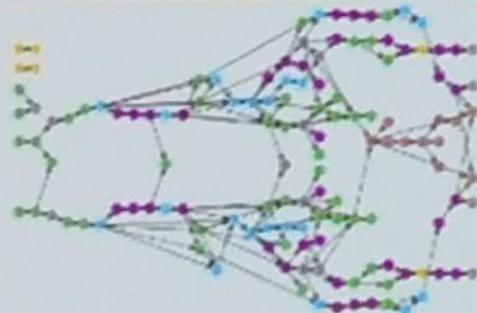


# Device targets for quantum supremacy

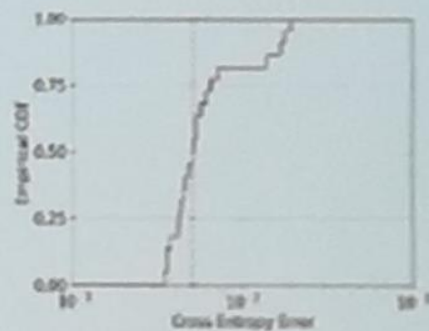
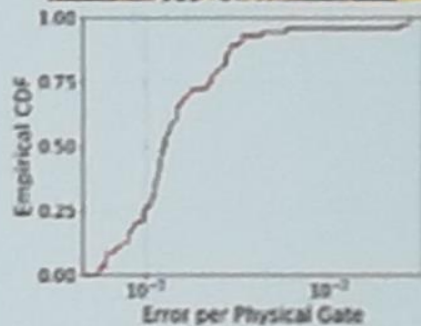
50+ qubits



99.9% single qubit gates

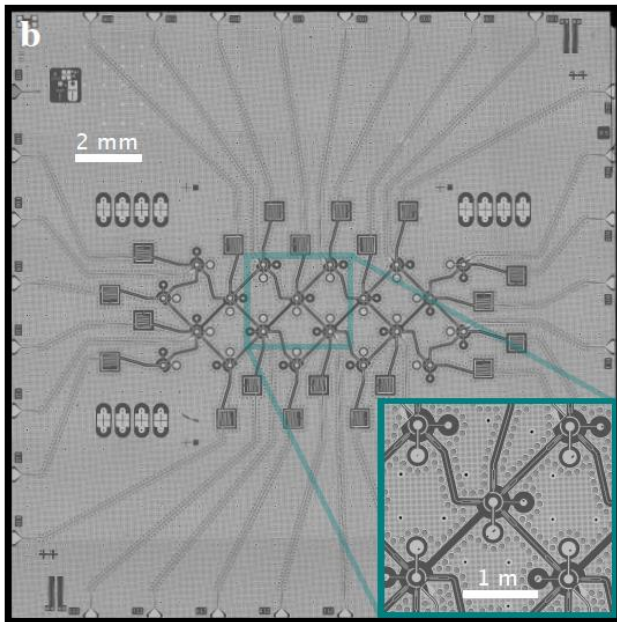
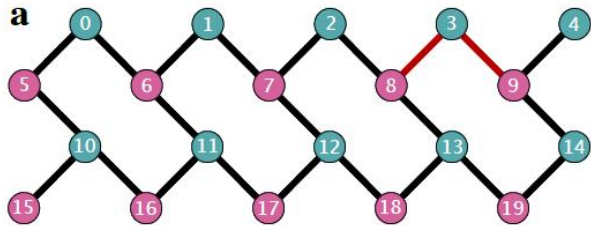


99.5% two qubit gates

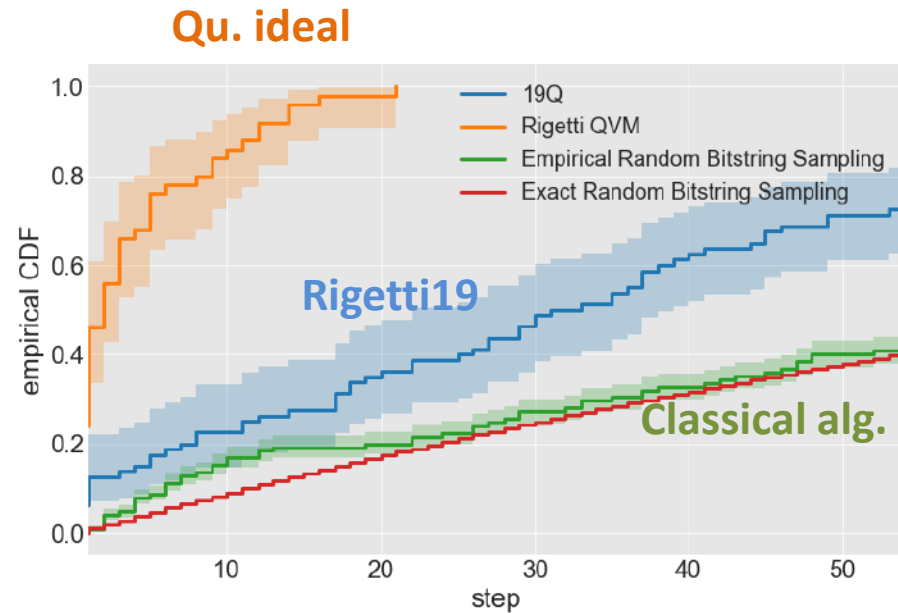


# A hot subject: quantum machine learning

Rigetti19 qubit circuit (2017)



Otterbach et al.,  
PRL 2018 & [arXiv:1712.05771](https://arxiv.org/abs/1712.05771)



# The scalability challenge

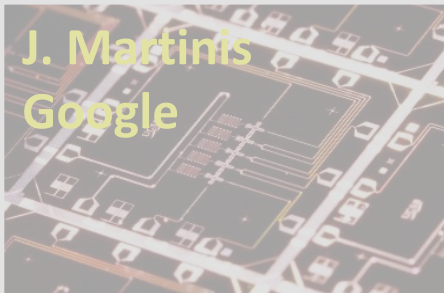
## GATE BASED PROCESSORS

### Elementary processors

Better coherence:  
 $T_2$ : 10-30  $\mu$ s

Protocols :  
teleportation,  
quantum feedback  
Digital simulation

### The NISQ era



Quantum advantage  
within reach ?

### Addressing Quantum Error Correction

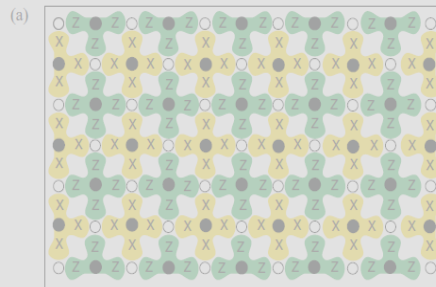
Beware: copying forbidden!

fault-tolerant architectures

surface code fabric

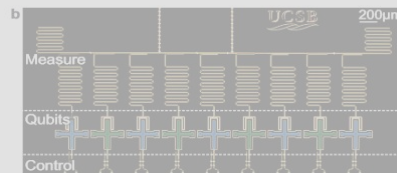
(see Fowler et al, PRA 86 (2012))

Data  measurement 



Huge resource overhead

1 logical qubit  $\gg$  1000 physical qubits



test circuits

for quantum error correction

robust qubits

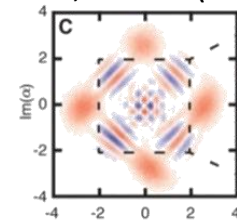
### Autonomous qubits

Yale Quantlab, M. Devoret  
INRIA- LPA (ENS)

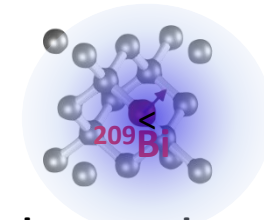
ENS Lyon (B. Huard)  
Schrödinger cat states in  
high Q resonators

Mirrahimi et al.

NJP 16, 32014 (2014)



### hybrids: spins in circuits

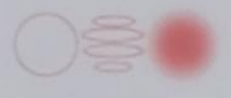


better coherence

## Cat-qubits: definition

Cat qubits based on coherent states of a high Q harmonic oscillator maintained alive using dissipation engineering

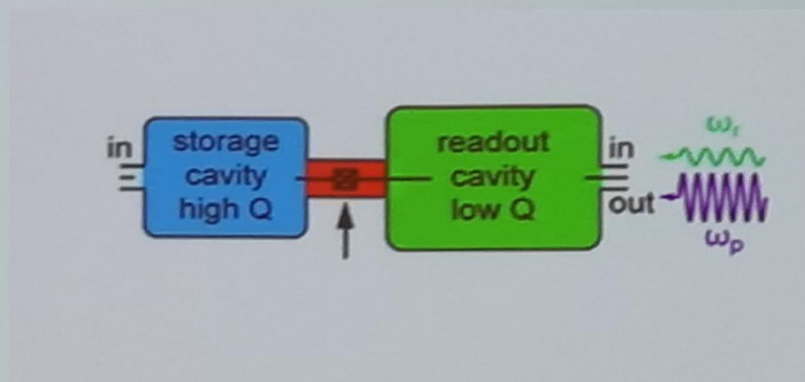
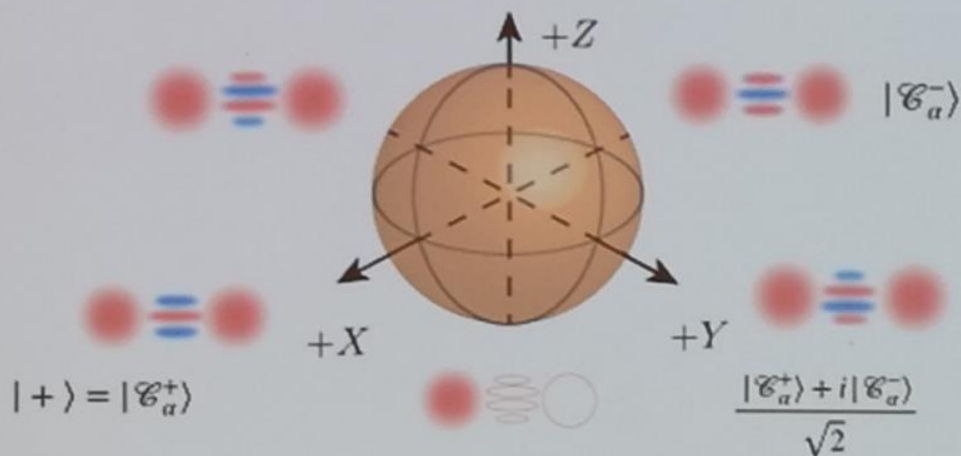
$$\alpha = \pm \sqrt{\frac{2\epsilon_{2ph}^*}{\kappa_{2ph}}}$$



$$|0\rangle = \frac{|\mathcal{E}_a^+\rangle + |\mathcal{E}_a^-\rangle}{\sqrt{2}}$$

$$|+\rangle = |\mathcal{E}_a^+\rangle \doteq \mathcal{N}_+(|\alpha\rangle + |-\alpha\rangle) = \sum c_{2n} |2n\rangle$$

$$|-\rangle = |\mathcal{E}_a^-\rangle \doteq \mathcal{N}_-(|\alpha\rangle - |-\alpha\rangle) = \sum c_{2n+1} |2n+1\rangle$$

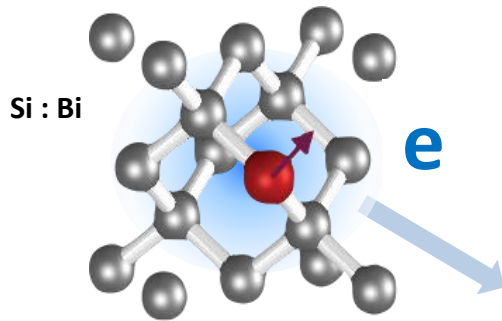


# A new hybrid route : spins coupled to superconducting circuits

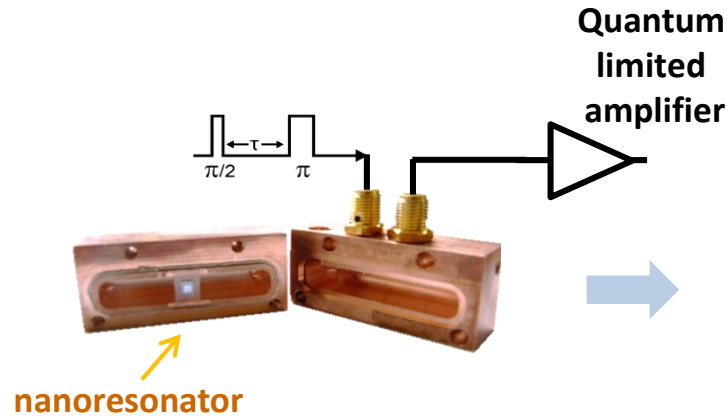
Nuclear spins

Electronic spins

hyperfine coupling



low mode volume high Q  
resonators



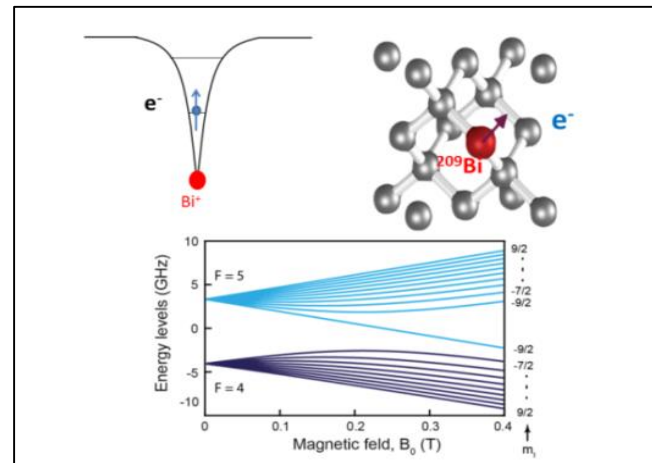
hybrid  
architecture ?

Highly coherent  
quantum system

- Electronic spin = 1/2
- Nuclear spin I=9/2
- Large hyperfine coupling  $\frac{A}{2\pi} = 1.4754\text{GHz}$

$$\frac{H}{\hbar} = \mathbf{AI} \cdot \mathbf{S} + \mathbf{B}_0 \cdot (-\gamma_e \mathbf{S} - \gamma_n \mathbf{I})$$

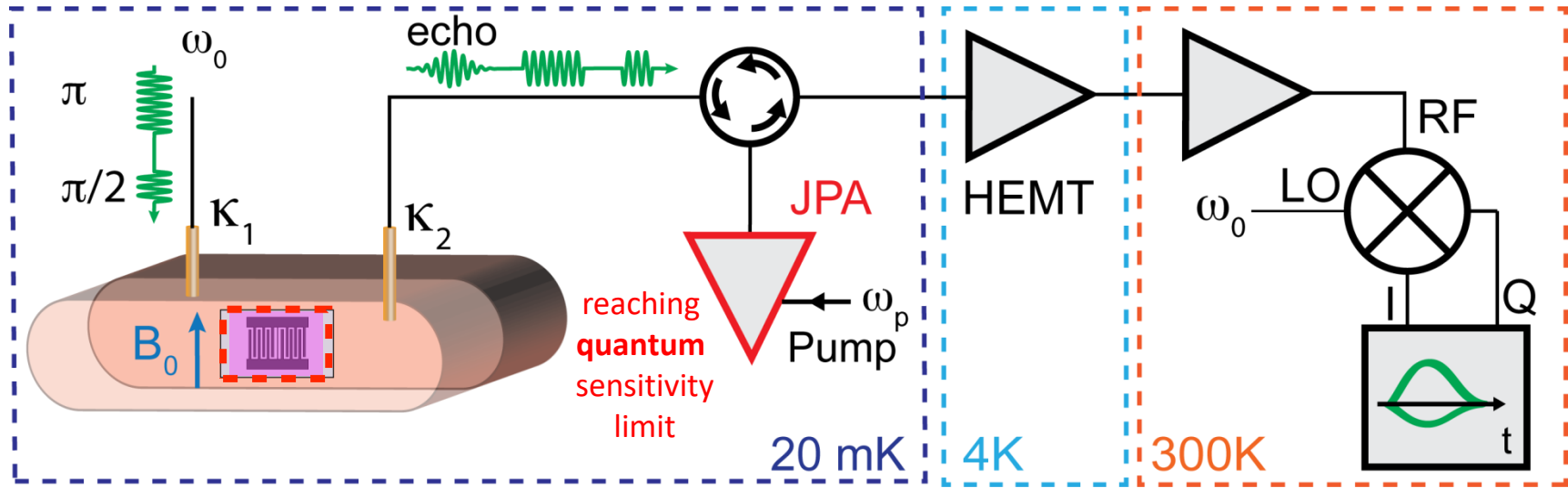
20 electro-nuclear states  
for making qubits



**Preliminary  
work**

# Ultra-sensitive ESR

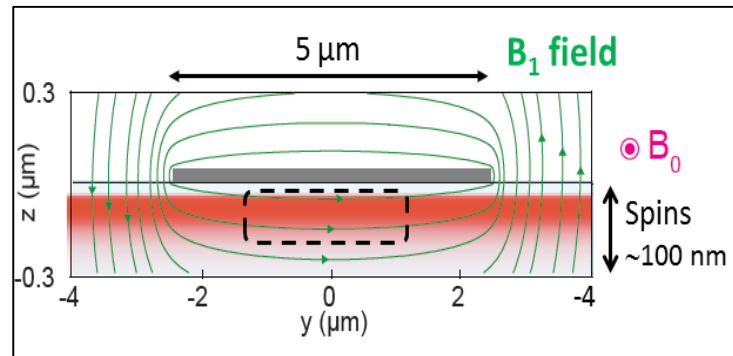
Bienfait et al.,  
Nature 2016, Nature Nano 2016,  
PRA 2017, PRX 2017; Probst et al., APL 2017



1.4 mm



2D high Q superconducting Al resonator

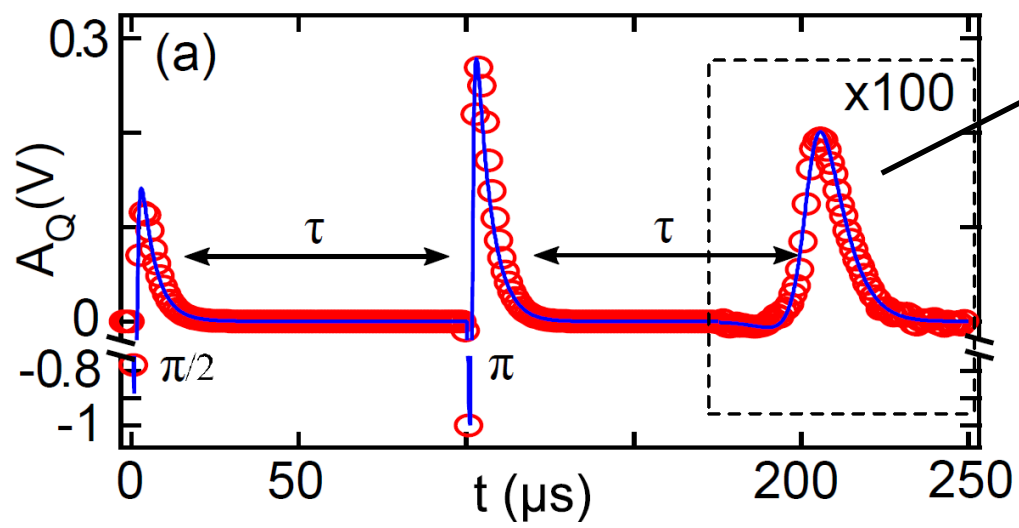
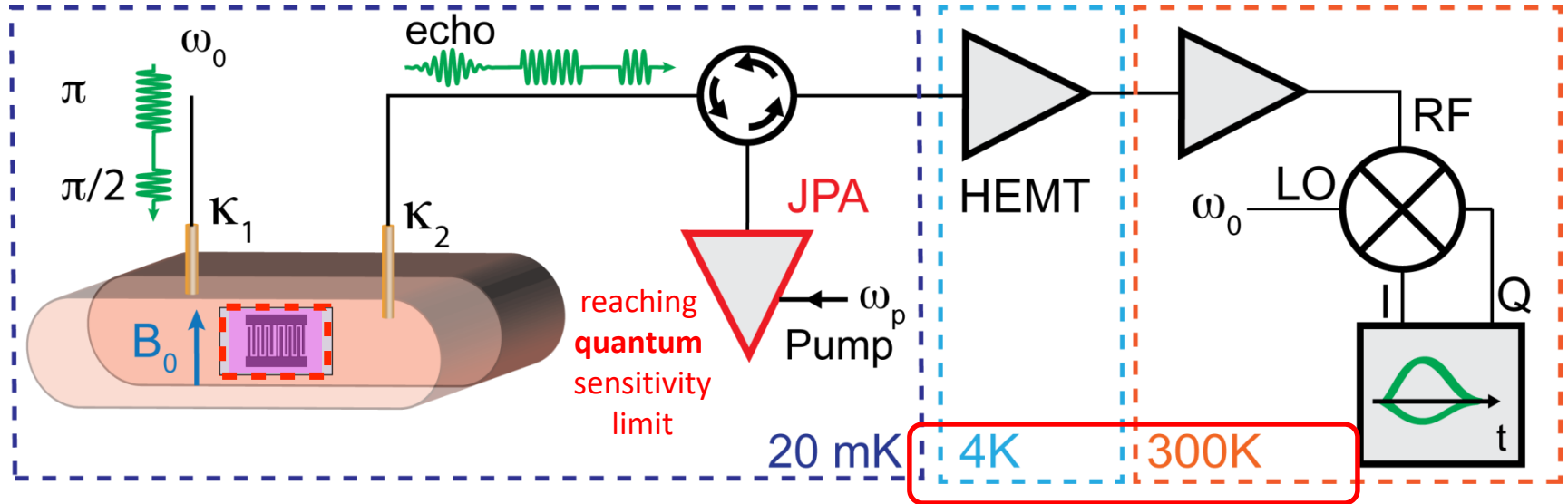


spins in small mode volume



# Ultra-sensitive ESR

Bienfait et al.,  
 Nature 2016, Nature Nano 2016,  
 PRA 2017, PRX 2017; Probst et al., APL 2017



250 spins/echo @SNR 1

NOW:  
 Effective detection sensitivity:  
 10 spins /  $\sqrt{\text{Hz}}$  @  $T_1 = 21 \text{ ms}$

*Toward single spin ESR*

# A new hybrid architecture based on nuclear spins

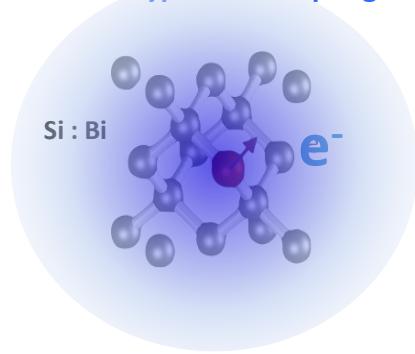
Nuclear spins

Electronic spins

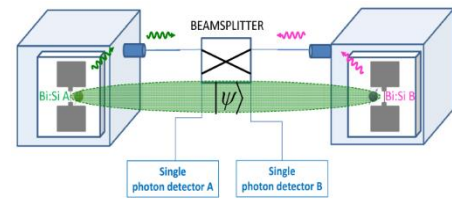
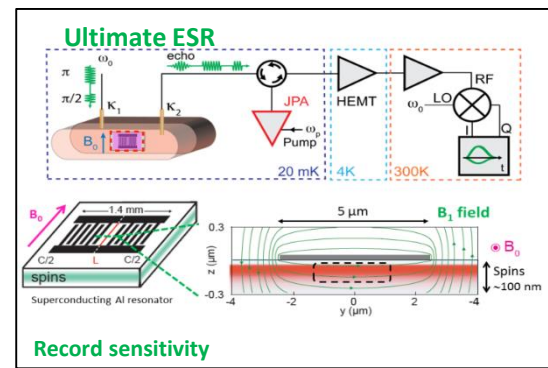
Superconducting quantum circuits

Gate & readout schemes

Hyperfine coupling

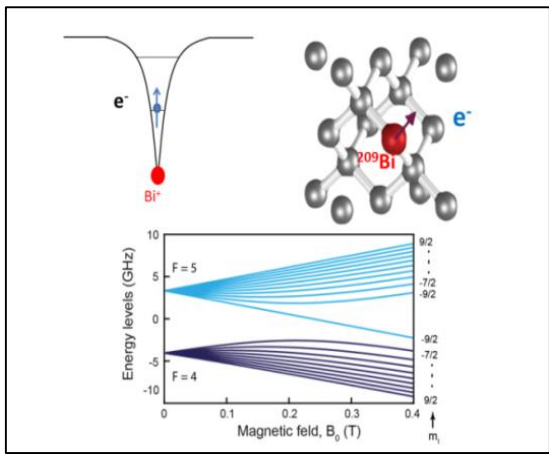


High Q low mode volume resonators, quantum amplifiers,...

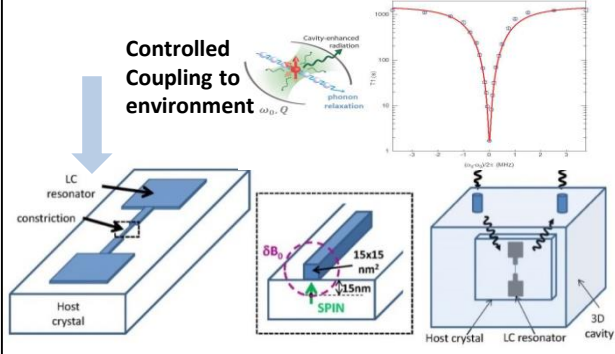


e.g. measurement based entanglement

**A route towards more robust quantum bits**



Controlled Coupling to environment



electronic spin-single microwave photon coupling

QUANTUM ELECTRONICS GROUP

AGENCE NATIONALE DE LA RECHERCHE  
ANR  
Atos

Chair

rich & coherent set of quantum levels



# Conclusions

-Interesting use-cases identified  
**many-body problem**, classification, ....  
but >100 logical (error corrected) qubits needed ...

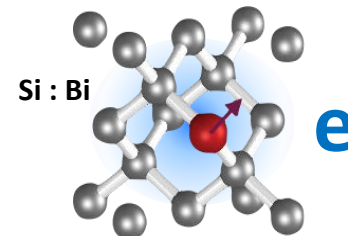
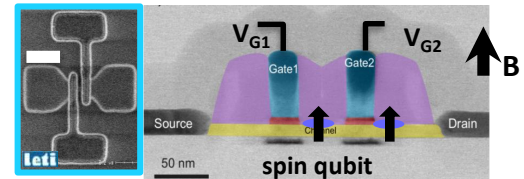
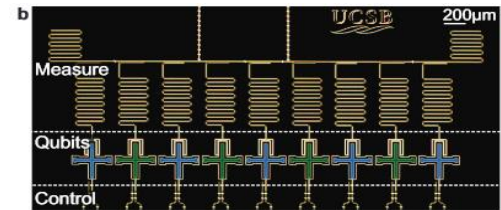
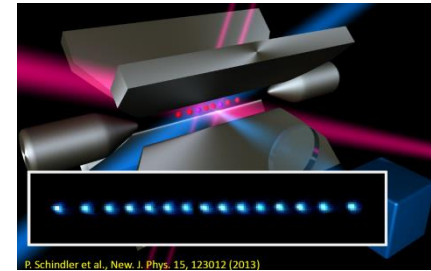
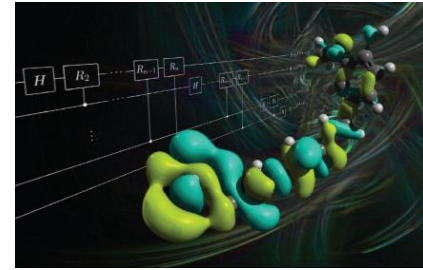


-Low depth processors at Google, IBM, Rigetti  
**targeting quantum advantage**  
-Sizeable progress on different platforms

-gate-based processors with quantum error correction  
**very difficult.**  
**Scalable fab. mandatory.**

-Other route **more coherent qubits**  
autonomous correction, Schröd. cat states, spins, ...

Appealing potential,  
but  
perspectives still unclear



# QUANTUM ELECTRONICS GROUP



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D. Vion



E. Flurin



& V. Ranjan, M. Rancic, M. Lee, F. Barbosa, B. Albanese, E. Albertinale, M. le Dantec & former members: A. Bienfait, S. Probst, X. Zhou



European Research Council